TITLE: METHODS FOR MAKING NEARLY PLANAR DIELECTRIC FILMS IN INTEGRATED CIRCUITS
INVENTORS NAME: Werner Juengling
DOCKET NO.: 303.510US2

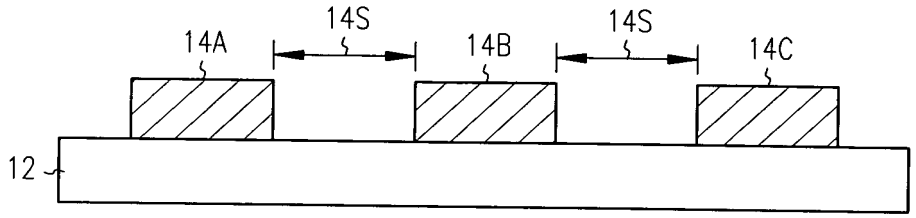

FIG. 1

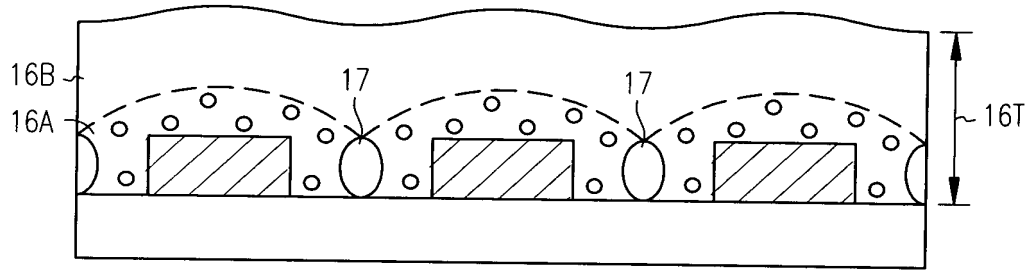

FIG. 2

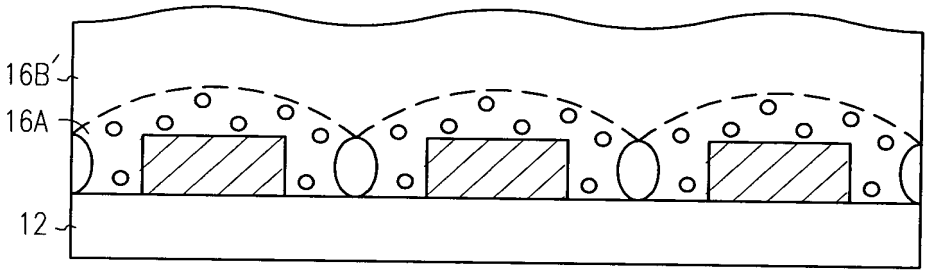

FIG. 3

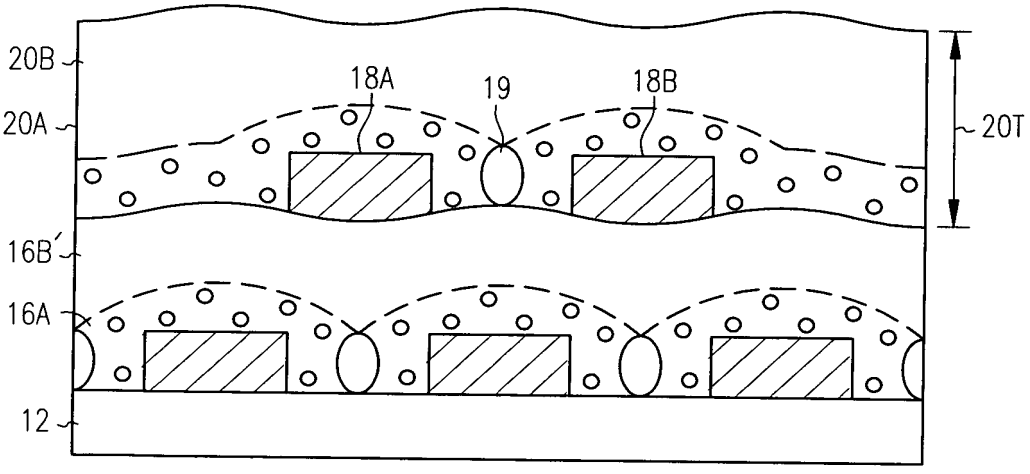

FIG. 4

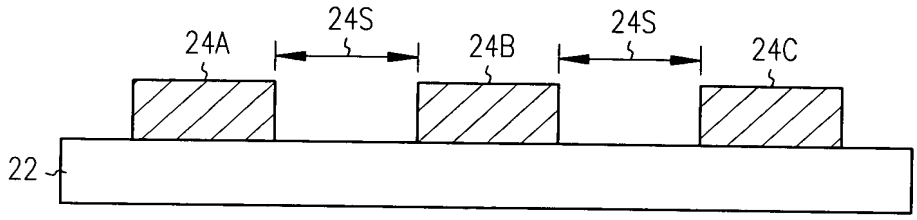

FIG. 5

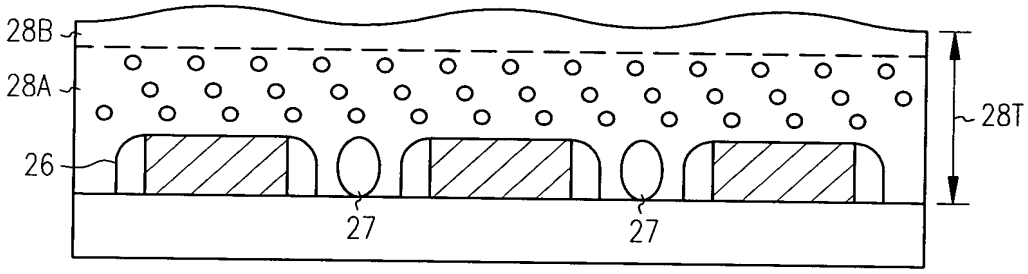

FIG. 6

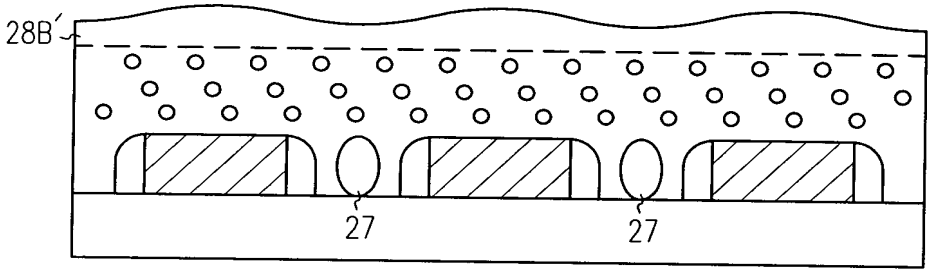

FIG. 7

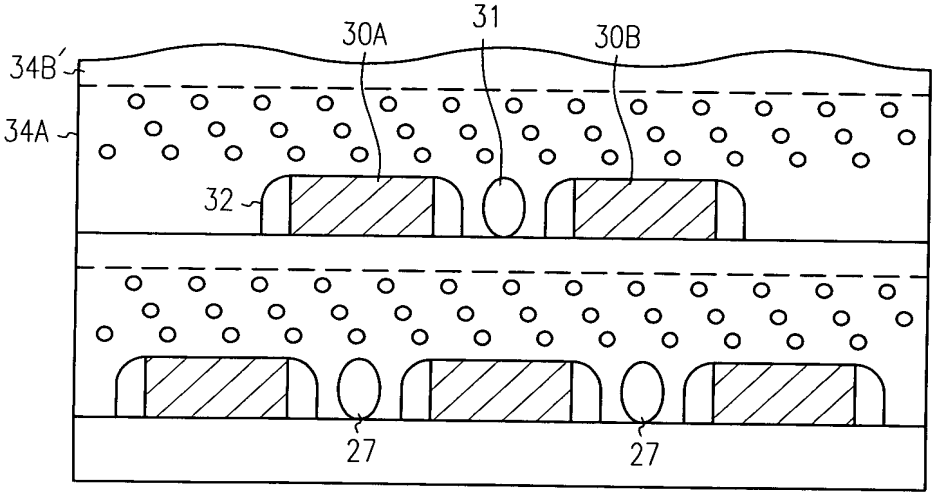

FIG. 8

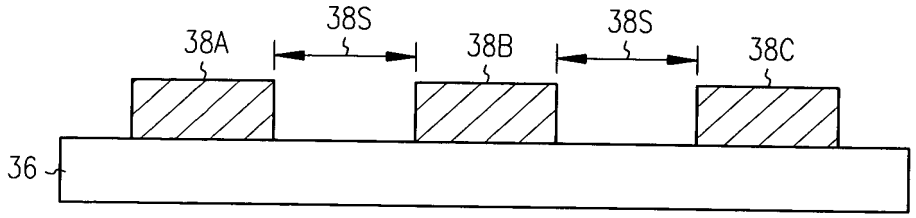

FIG. 9

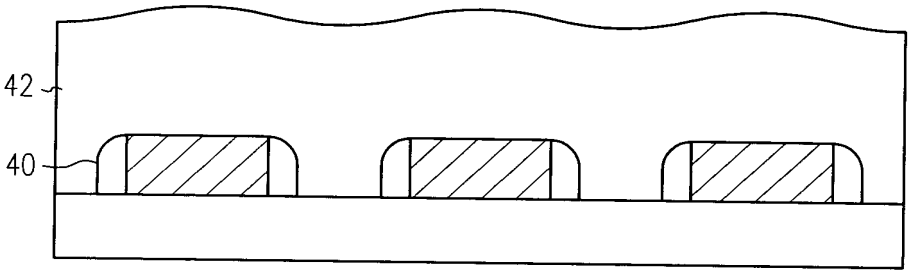

FIG. 10

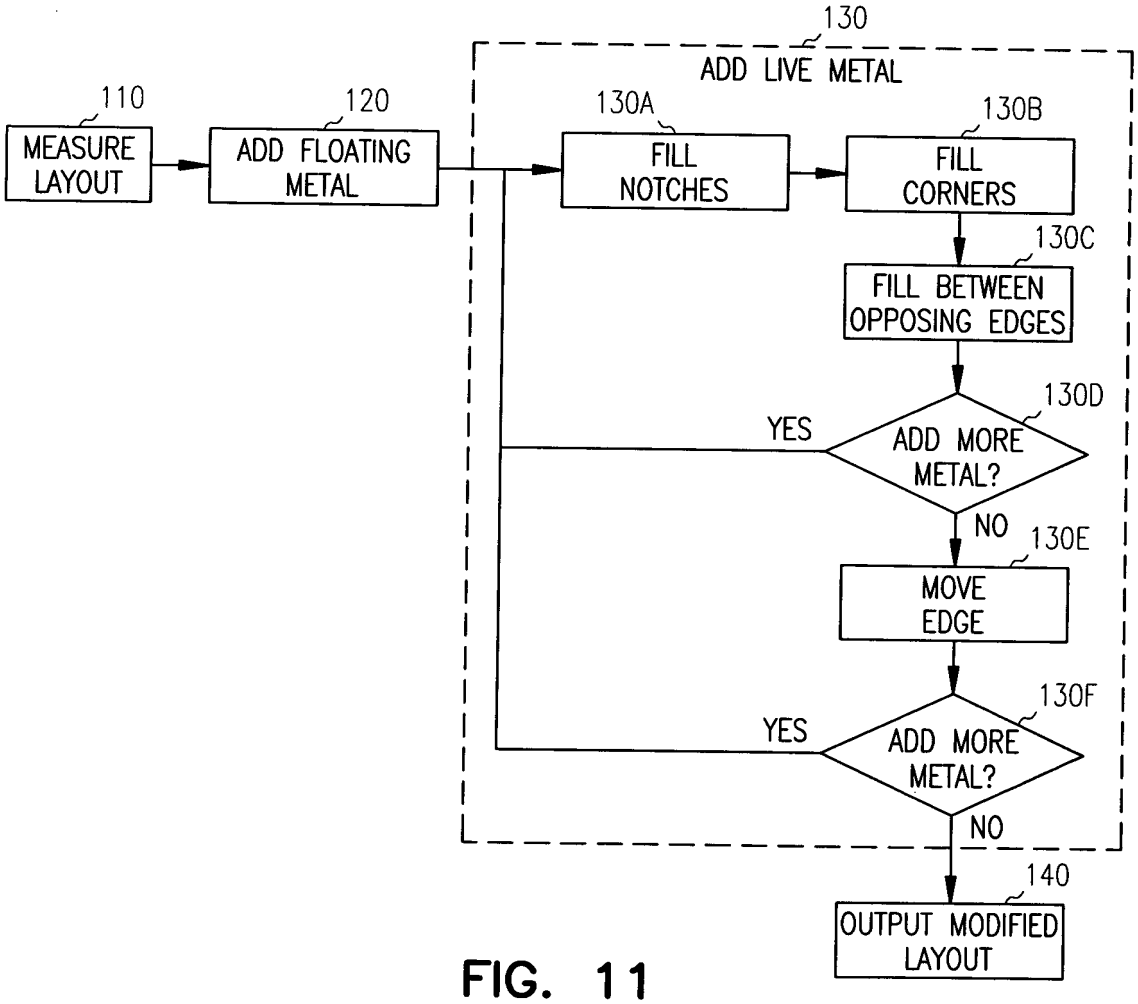

FIG. 11

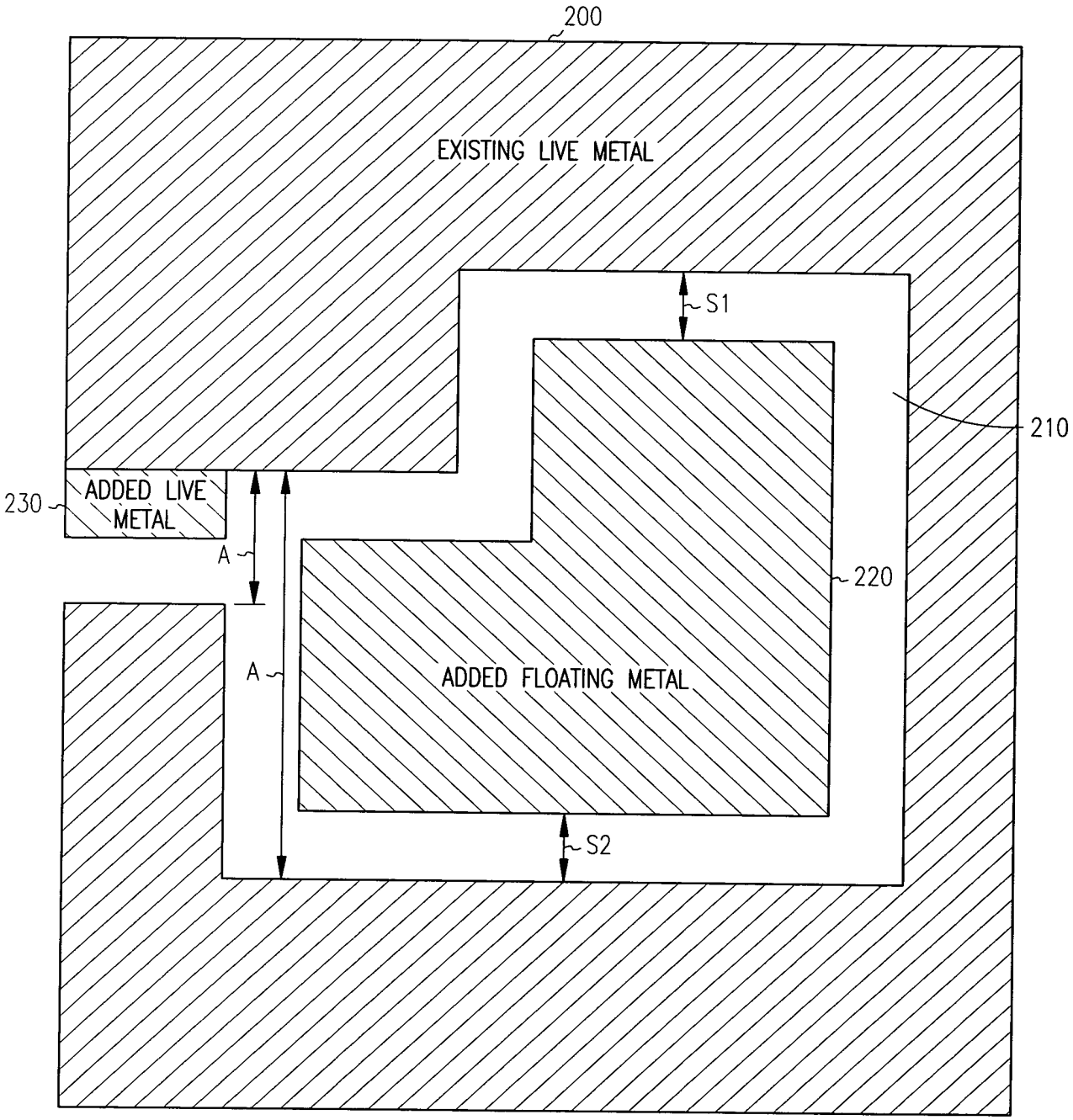
200
EXISTING LIVE METAL
S1
210
230
ADDED LIVE METAL
A
220
A
ADDED FLOATING METAL
S2

FIG. 12

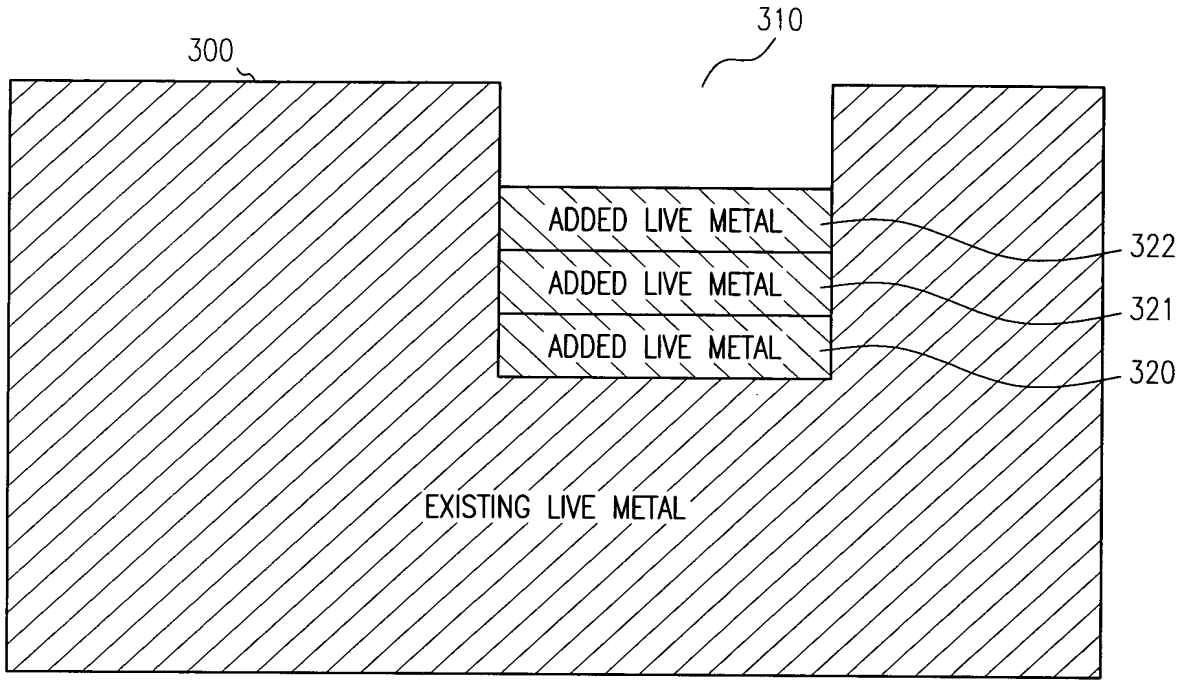
300
310
ADDED LIVE METAL
ADDED LIVE METAL
ADDED LIVE METAL
322
321
320
EXISTING LIVE METAL

FIG. 13

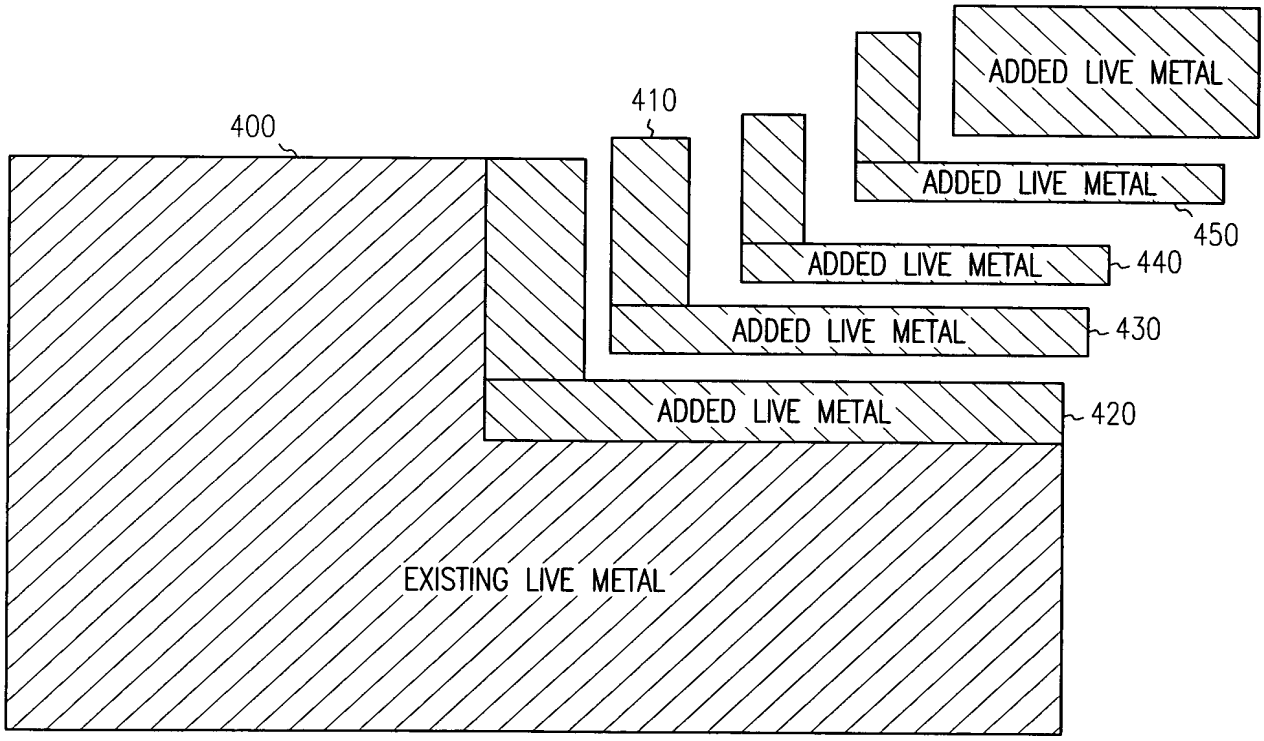
400
410
ADDED LIVE METAL
ADDED LIVE METAL
450
ADDED LIVE METAL
440
ADDED LIVE METAL
430
ADDED LIVE METAL
420
EXISTING LIVE METAL

FIG. 14

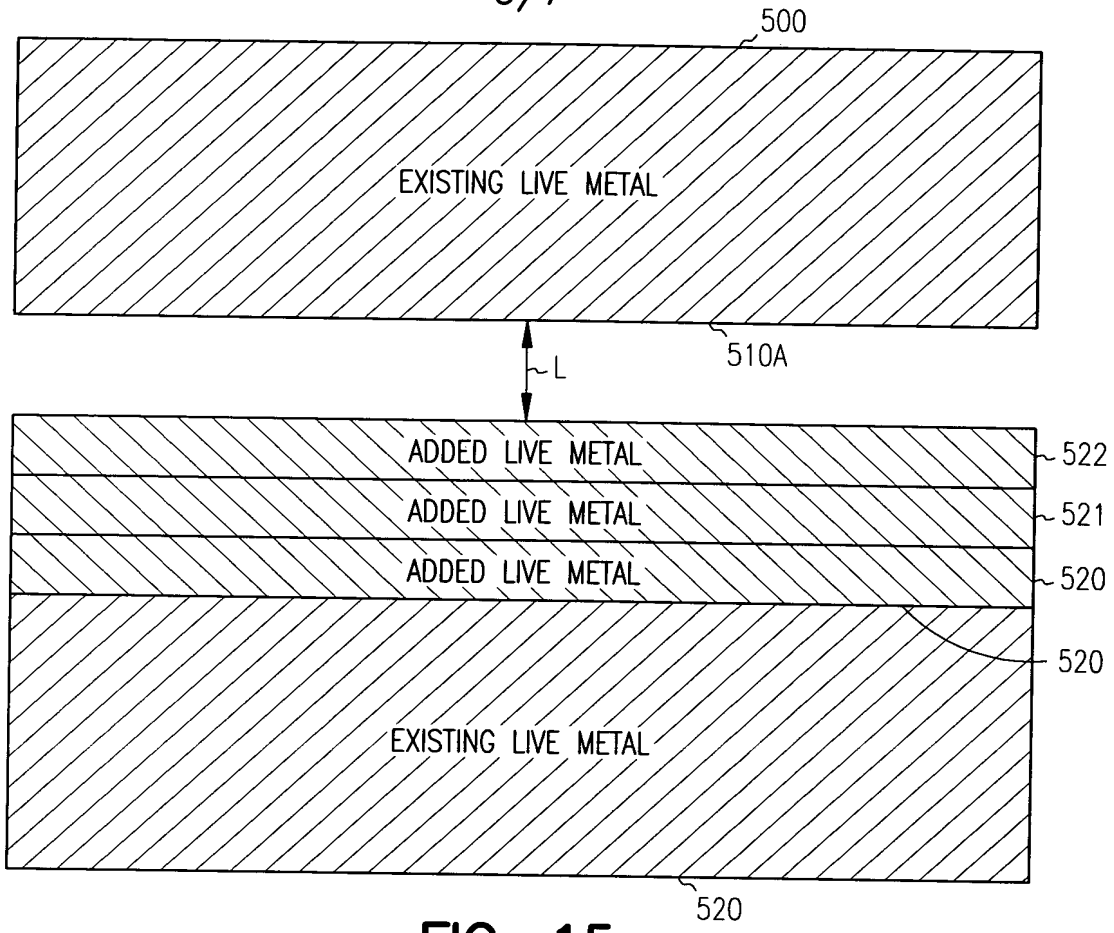
500
EXISTING LIVE METAL
510A
L
ADDED LIVE METAL
ADDED LIVE METAL
ADDED LIVE METAL
522
521
520
520
EXISTING LIVE METAL
520

FIG. 15

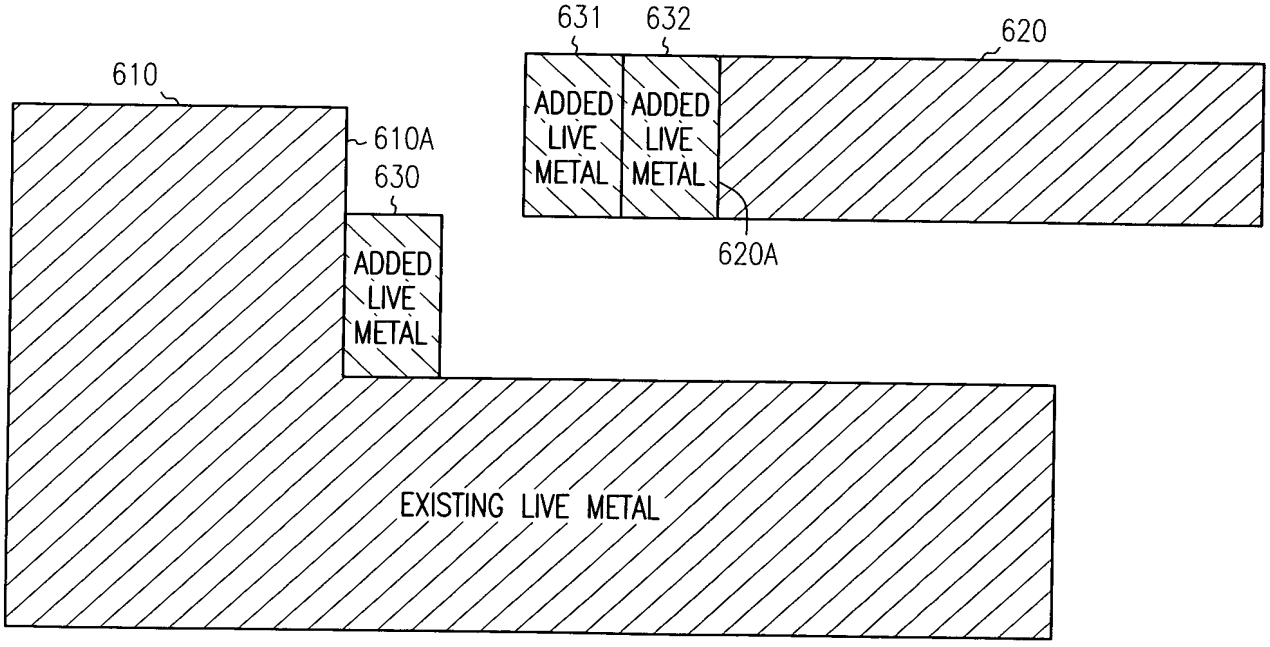
631
632
620
610
610A
630
ADDED LIVE METAL
ADDED LIVE METAL
ADDED LIVE METAL
620A
EXISTING LIVE METAL

FIG. 16

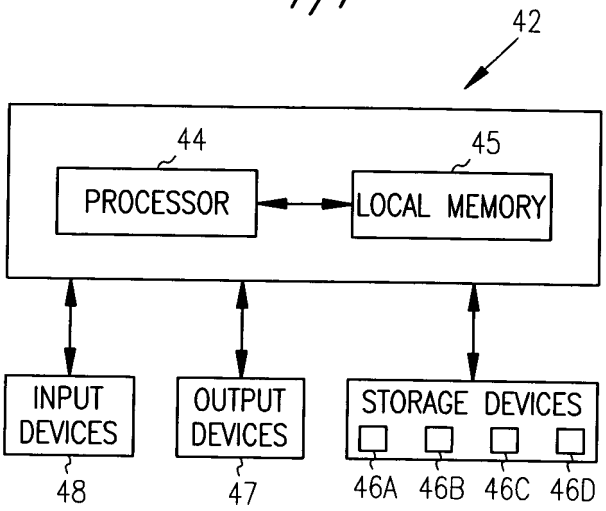
42
44
45
PROCESSOR
LOCAL MEMORY
INPUT DEVICES
OUTPUT DEVICES
STORAGE DEVICES
48
47
46A
46B
46C
46D

FIG. 17

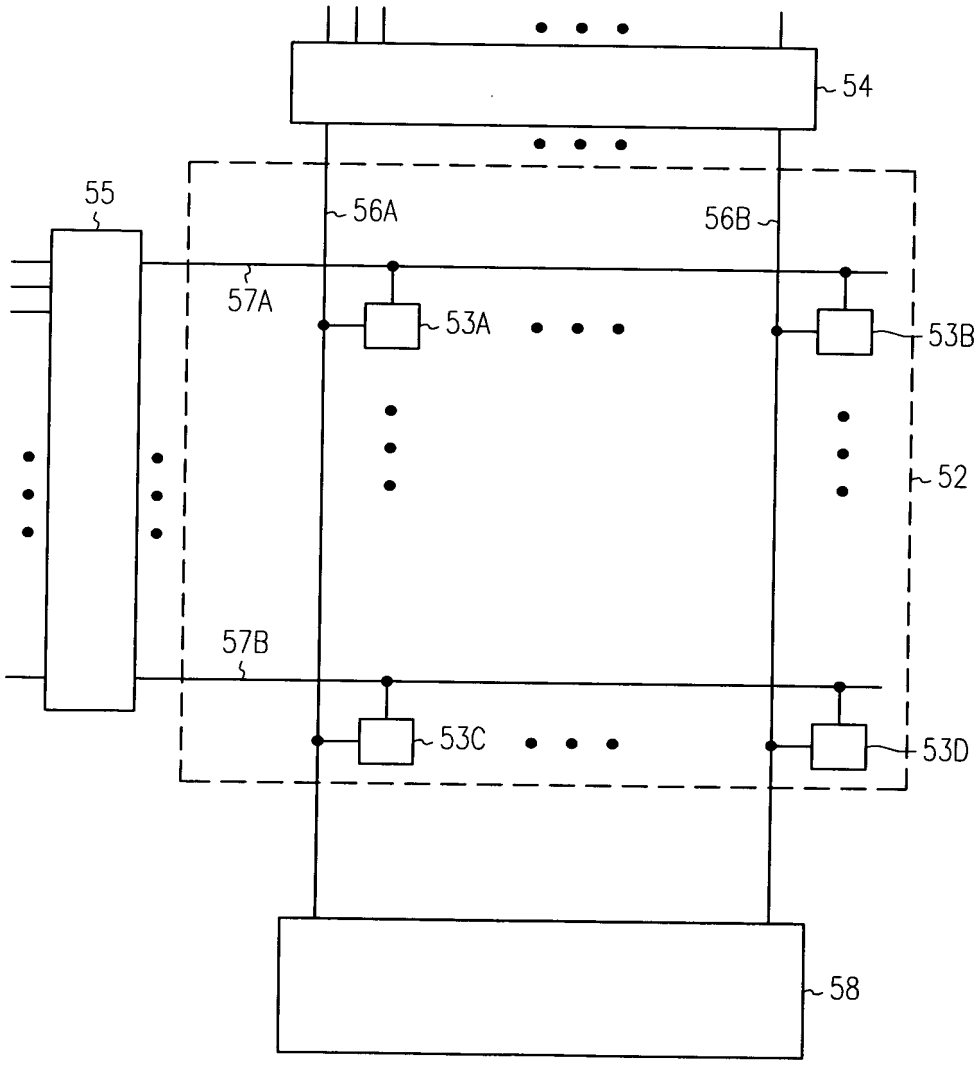
54
55
56A
56B
57A
53A
53B
52
57B
53C
53D
58

FIG. 18